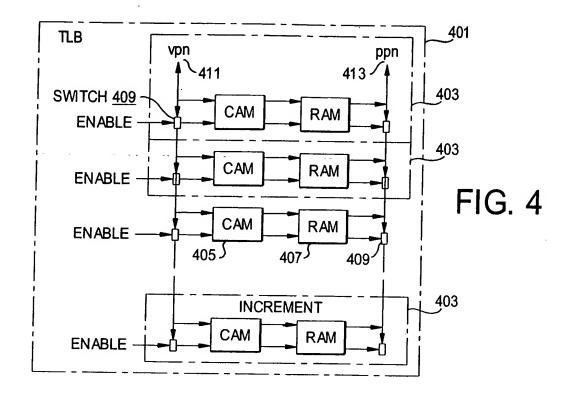


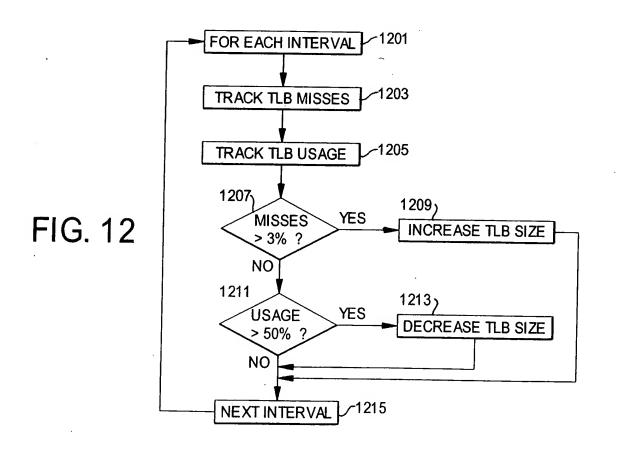
FIG. 2

SUBARRAY / WAY ALLOCATION (L1 OR L2)

		က	I	<u>۰</u>		\ \alpha\!	T 0:	٦.	Τα.	1/	
	SUBARRAY 3	W3	2	2	12	7	2	7	2	1/2	
		<u>8</u>	2	7	2	2	2	2	17	1/4	
		₹	7	12	12	2	12	17	17	1/2	
٠.		8	12	[2]	12	7	37	17	17	Z	
	SUBARRAY 1	W3	2	7	2	17	77	27	2	1	
		W2	7	12	12	17	17	12	12	1/2	
		W V	12 12 12	77	17	17	7	777	17	//	
		W OW	777	177	77	177	777	77	17	17	
			75	17	755	15	155	177	15	17	
ı	SUBARRAY 0	W .	12/	(32)	17	17	12	17	121	1/2	
		W1	2	17	13	ZZ,	27	7	[ <del>]</del>	13	
		W2	7	12	17	(37)	12	7	17	7	
		W3	7	12	77	17	12	12	12	1/2	
ľ	SUBARRAY 2	<u> </u>	17	12	7	77	12/	177	17.	17	
		W1 W0	2		L2 L2		77	77	77	17	
1		<u> </u>	ר	12		7	ב	13	72/	72	
		W2	L2 L2	77	77	12	12 12	77	12/	14	
		W3	12	77	17	12	12	12	12	1	
	ACC A	WE :	2.0	2.5	2.5	3.0	3.0	3.5	4.0	4.5	
•	<b>⊿</b> ₹	: =	``		,,			(.,	7	7	
		ဗ	¥	¥	ΑY	ΑY	≽	4	ΑΥ	<u>}</u>	
	$\sum$	ASSOC	1 WAY	2 WAY	3 WAY	4 WAY	1 WAY	2 WAY	3 WAY	4 WAY	
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		اس	<u>ھ</u>	<u>ھ</u>	<u>æ</u>	<u>æ</u>	<u>ھ</u>	<u>ھ</u>	æ	9	
	7	SIZE	256KB	512	768KB	024	512KB	024	536	048	
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			256-1	512-2 512KB	768-3	1024-4 1024KB	512-1	1024-2 1024KB	1536-3 1536KB	2048-4   2048KB	
			- •		• -	•		7	7	×	
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						CACHE CONFIGURATION					
						S G	2				
			O EN								
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FIG. 3





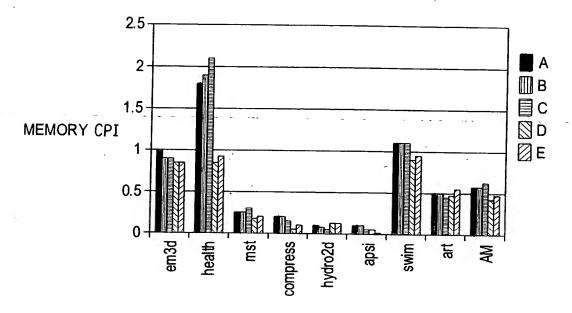
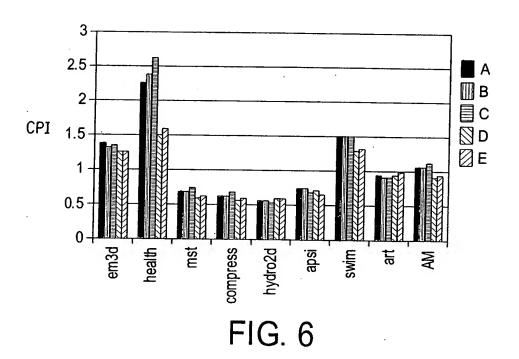


FIG. 5



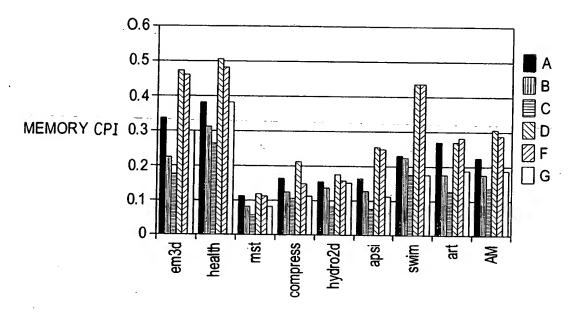
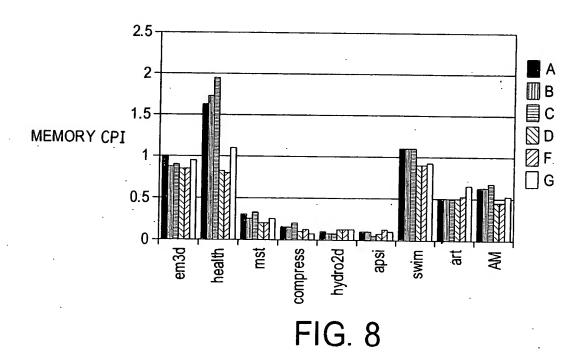
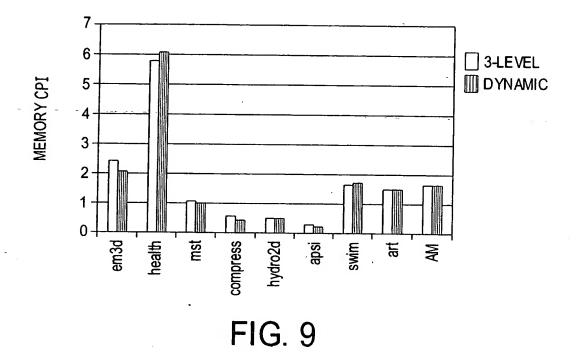
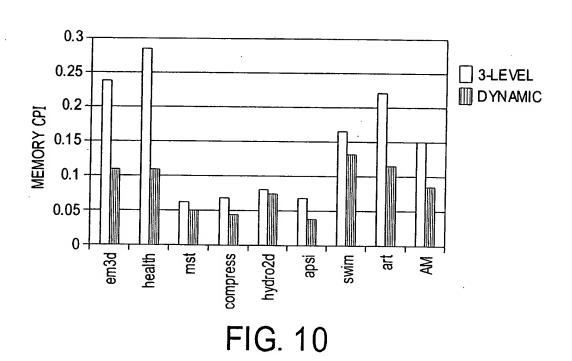


FIG. 7







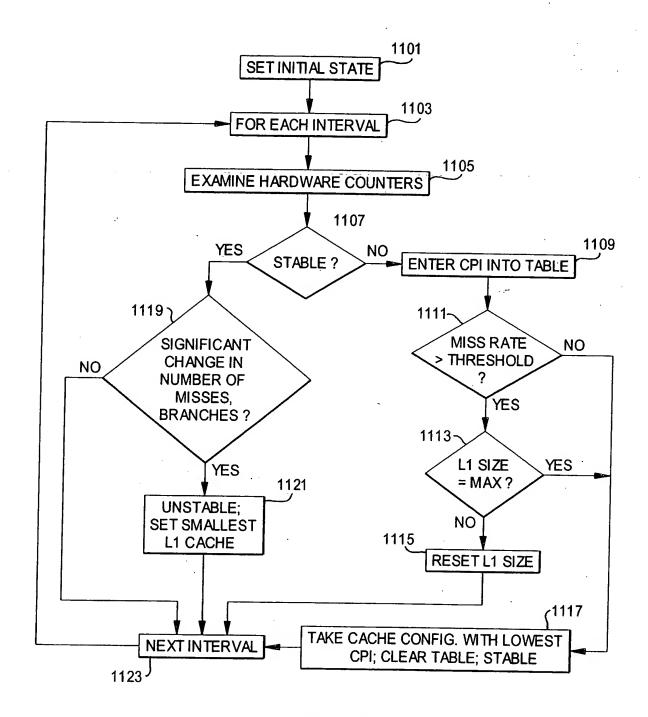


FIG. 11